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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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TECHNOLOGY CENTER 2800

In re application of

Docket No: Q65962

Katsuji KIMURA

Appln. No.: 09/940,472

Group Art Unit: 2816

Confirmation No.: 4891

Examiner: Minh T. NGUYEN

Filed: August 29, 2001

For: LINEAR VOLTAGE SUBTRACTOR/ADDER CIRCUIT AND MOS DIFFERENTIAL
AMPLIFIER CIRCUIT THEREFOR

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated September 24, 2002, please amend the above-
identified application as follows:

IN THE CLAIMS:

Please cancel claims 5-13 without prejudice or disclaimer.